J. Conor Schehl

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EDUCATION

Colorado School of Mines, Golden, CO - Bachelor of Science, Electrical Engineering
—**Expected Graduation: May 2028**

- GPA: 4.0
- Intent on a MS in Engineering concentrated on Integrated Circuits & Systems

SKILLS

- **Hardware**: PCB Design (Altium Designer ECAD/EDA), Schematic Capture, STM32, SMD/SMT Soldering
- **Firmware**: Python, C++, Linux (Debian), STM32
- **General**: Microsoft Suite, Google Suite, Team Collaboration, Design Reviews, SolidWorks

EXPERIENCE

Formula Society of Automotive Engineers | Electrical Engineer | **September 2025 - Present**

- Collaborate with a multidisciplinary team to make formula-style racecars perform competitively at the collegiate level
- Design and assemble PCBs and Wire Harnesses to acquire telemetry, engine, and car data to maximize performance while adhering to safety regulations

Rosendin | Engineer Intern | **May 2026 - August 2026**

- Conducted thorough short circuit and arc flash analysis for power distribution systems, ensuring strict protocol and safety compliance
- Planned system coordination and developed a disciplined approach to power management, fault isolation, and system reliability

PROJECTS

Custom STM32 Microcontroller Development Board, Mines Formula

- Architected a 4-layer development board with pin exposure for Arduino Shield compatibility and testing via Serial Communications like CAN, SPI, I^2C, and UART
- Adhered to >80% power conversion efficiency (12V to 3.3V) using switching and linear regulators isolating the switching converter from sensitive analog traces to minimize EMI
- Optimized high-speed signal integrity by implementing differential pair routing for USB 2.0 and CAN, and by designing a precise layout for the High-Speed Oscillator for robust timing stability across temperature variances

Analog to Digital Converter Data Acquisition and Power Distribution Board, Mines Formula

- Developed a mixed-signal ADC DAQ board using an STM32 MCU and a dedicated ADC communicating over SPI, implementing low-pass input filters for signal integrity
- Managed PCB power budget and thermal dissipation for analog and digital sensor loads
- Engineered high speed Ethernet communication with the Vehicle Control Unit with an Ethernet PHY

Power and Safety Circuit Latching Board, Mines Formula

- Designed an analog safety board utilizing relays, optocouplers, diodes, and fuses, to provide reliable voltage switching, galvanic isolation, and safe EV shutdown